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(71) Applicant: TEXAS INSTRUMENTS INC.  
Dallas, Texas 75243 (US)

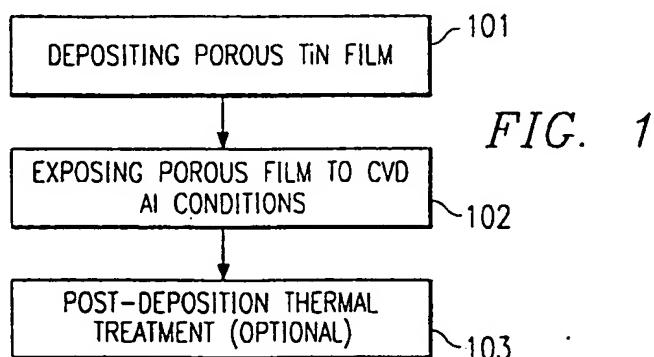
(72) Inventors:  
• Lu ,Jlong - Ping  
Dallas Texas 75243 (US)  
• HSU, Wei-Yung  
Dallas Texas 75243 (US)  
• Hong , Qi-Zhong  
Dallas Texas 75243 (US)

(74) Representative: Williams, Janice et al  
D. Young & Co.,  
21 New Fetter Lane  
London EC4A 1DA (GB)

### (54) Method of forming films comprising TiN and Al

(57) A process for producing conformal and stable TiN + Al films, which provides flexibility in selecting the chemical composition and layering. In this new process,

porous TiCN is first deposited, and then Al is incorporated by exposing the porous film to CVD aluminum conditions at low temperatures.



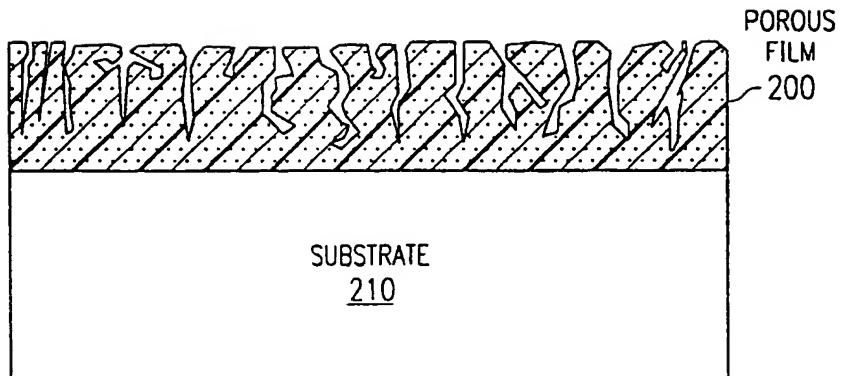


FIG. 2A

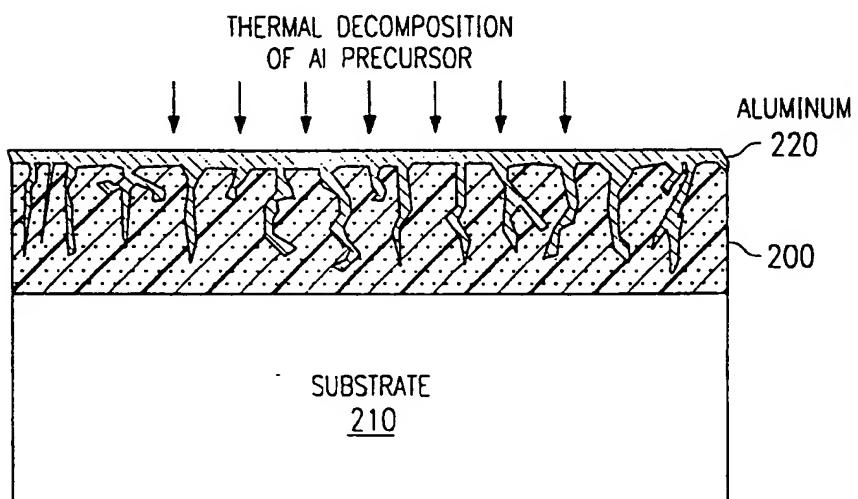


FIG. 2B

**Description****BACKGROUND AND SUMMARY THE INVENTION**

5 The present invention relates generally to integrated circuit structures and fabrication methods. Ever since the mid-1980s, titanium nitride (TiN) has been a very common material for diffusion barrier layers in integrated circuit fabrication. However, with the increased demands on metallization and interconnect technology in the late 1990s, there is an increasing need for thin film structures with better diffusion-barrier and adhesion properties.

10 Ti-A1-N films have been proposed as one alternative. Ti-A1-N films are attractive for diffusion barriers in integrated circuit fabrication. The use of aluminum in the composition (instead of a pure TiN composition) produces an amorphous thin film, which is desirable for a diffusion barrier (since diffusion pathways along grain boundaries are eliminated). In such applications the A1 fraction will form a thin but durable native oxide (which is primarily  $\text{Al}_2\text{O}_3$ ), so that the material (like aluminum metal, but unlike TiN) self-passivates itself on exposure to air. See generally, e.g., Lee et al., "( $\text{Ti}_{1-x}\text{Al}_x$ )N coatings by plasma-enhanced chemical vapor deposition," 12 J. VAC. SCI. TECHNOL. A 1602 (1994); Wahlström et al., "Crystal growth and microstructure of polycrystalline  $\text{Ti}_{1-x}\text{Al}_x\text{N}$  alloy films deposited by ultra-high-vacuum dual-target magnetron sputtering," 235 THIN SOLID FILMS 62 (1993); Lee and Lee, "Compositionally gradient ( $\text{Ti}_{1-x}\text{Al}_x$ )N coatings made by plasma enhanced chemical vapor deposition," 13 J. VAC. SCI. TECHNOL. A 2030 (1995); Inoue et al., "Structure and composition of (Ti, A1)N films prepared by r.f. planar magnetron sputtering using a composite target," 271 THIN SOLID FILMS 15 (1995).

20 Ti-A1-N films are also attractive for hard coatings for solid objects. Titanium nitride is often used for a friction-reducing or decorative layer, but is not stable against oxidation, and hence some protective overcoat is usually used. However, this increases the cost of such films, and may also reduce their durability.

25 Ti-A1-N films are normally made by one of two methods: reactive sputtering (PVD) using Ti-A1 alloy targets in a nitrogen atmosphere; or by CVD using  $\text{TiC}_{14} + \text{NH}_3 + \text{AlC}_{13}$ . Reactive sputtering processes suffer from poor step coverage and limited range of A1-Ti ratio, while CVD processes require high substrate temperatures during deposition (which is a problem for multilayer metallization). CVD processes also risk a corrosive residue (e.g. C1) and particle generation due to gas phase reactions.

**SUMMARY OF THE INVENTION**

30 Respective aspects of the present invention are set forth in claims 1, 7, and 13.

35 Embodiments of the invention introduce new processes for producing films containing titanium, aluminum, and nitrogen that overcome the shortcomings of the other processes outlined above. In the disclosed new processes, porous TiN is first deposited, and then A1 is deposited onto and into the pores of the porous film. This produces a multilayer highly planar film which has an aluminum-rich surface layer (which may be metallic aluminum, or aluminum nitride, or a mixture thereof). Upon exposure to atmosphere, this will form a hard oxide film which passivates the film against further oxidation. The films made by this method can be given different chemical compositions as compared with other methods, and also offer additional advantages.

40 The new TiN+A1 film structures provide easier deposition than conventional Ti-A1-N films. As compared with PVD methods for Ti-A1-N film deposition, the new TiN+A1 deposition methods give better step coverage, and permit a wider range of chemical composition. As compared with the  $\text{TiC}_{14}\text{+AlC}_{13}\text{+NH}_3$  CVD processes for Ti-A1-N film deposition, the new TiN+A1 deposition methods give reduced particulates, allow lower deposition temperature, and do not leave any corrosive residue.

45 Thus the disclosed methods provide the advantages of:

- (i) good step coverage;
- (ii) minimized particle generation;
- (iii) flexibility in selecting chemical composition; and
- (iv) high manufacturability.

50 The structures produced by methods embodying the invention are particularly useful for diffusion barriers for multilevel interconnect technology, and for capacitor plates in high density DRAM storage capacitors with perovskite oxide dielectrics (e.g. barium strontium titanate or PLZT). The disclosed methods can also advantageously be used to deposit a self-passivating hard coating on solid objects.

**BRIEF DESCRIPTION OF THE DRAWINGS**

55 Embodiments of the invention will be described herein, by way of example, with reference to the accompanying

drawings, in which:

- 5 Figure 1 shows a process flow for preparing the film;
- Figure 2A shows a porous as-deposited film;
- Figure 2B shows the film of Figure 2A after the aluminum treatment;
- Figure 3 shows XPS characterization results of the film of Figure 2B;
- Figure 4 presents data showing how a TiN layer nucleates deposition of aluminum;
- 10 Figures 5A and 5B show an example of metallization over a barrier film layer deposited by a process embodying the invention;
- Figure 5C shows yet another metallization embodiment which uses the TiN-A1 films as diffusion barrier for a DRAM capacitor structure; and
- Figure 6 is a micrograph showing a diffusion barrier fabricated by the methods of Figure 1.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

15 The numerous innovative teachings of the present application will be described with particular reference to the presently preferred embodiment. However, it should be understood that these embodiments provide only a few examples of the many advantageous uses of the innovative teachings herein. Moreover, some statements may apply to some inventive features but not to others.

20 In general, particulate formation, during deposition of thin films containing titanium and nitrogen, can be reduced by incorporating aluminum after deposition of the TiN film without using  $TiC_4$ ,  $A_1C_3$  and  $NH_3$ . This process does not leave any corrosive residue, increases the stability of the TiN films, and offers excellent step coverage.

A sample process flow for fabricating TiN + A1 films is shown schematically in Figure 1. This process is carried out using conventional chemical vapor deposition (CVD) systems.

25 The first step (step 101) includes depositing a porous TiN film, preferably, via thermal decomposition of  $Ti(RR')_4$ , where R, R' can each be methyl or ethyl. This is a metal-organic chemical vapor deposition (MOCVD) with excellent step coverage for the resulting TiN film.

30 Following this deposition step (step 101), the porous film is exposed to CVD aluminum conditions (step 102) to introduce A1 into the film and onto the surface of the film. The CVD aluminum conditions are preferably applied for long enough to deposit at least 50 Å of metallic aluminum on top of the TiN layer (and more preferably 100 Å), as well as filling in any porosity or roughness in the TiN layer. The aluminum-rich surface layers will react with oxygen, on exposure to air, to form a durable native oxide (which is primarily  $A_1_2O_3$ ). Thus the layers formed by the disclosed methods will self-passivate on exposure to air (like aluminum metal, but unlike TiN).

35 The surface layer preferably has an A1 to Ti ratio of at least 5:1, and more preferably 10:1 or much higher (up to infinity). This is most easily achieved by performing aluminum deposition long enough that a thin layer of metallic aluminum is formed at the very surface. Alternatively and less preferably, if the surface can be sufficiently aluminum-enriched to form a hard alumina-like native oxide, this metallic layer can less preferably be made thinner or of zero thickness.

40 Optionally, the TiN+A1 film can be subjected to post-deposition thermal treatments (step 103) to further stabilize the barrier layer (by increasing the aluminum nitride zone).

MOCVD TiN has been shown to be an excellent nucleating layer for CVD A1, as shown in Figure 4. The data shows the reflectance of a 3000 Å CVD A1 film deposited on a 500 Å CVD TiN film as a function of the CVD TiN precoat thickness. This shows that a TiN layer will reliably nucleate deposition of aluminum. This nucleation facilitates the deposition of very smooth and very thin aluminum layers which is provided by the present invention.

45 Figure 2A shows a porous as-deposited film of TiN 200 over a substrate 210. Figure 2B shows the film of Figure 2A after the aluminum treatment. The aluminum 200 is deposited into the pores of the Tin film via thermal decomposition and produces a smooth, aluminum-enriched surface layer.

Films deposited using the process disclosed herein have excellent step coverage. This is a significant advantage over PVD processes.

#### 50 FIRST SAMPLE EMBODIMENT

A first sample embodiment performs the initial TiN deposition from a tetrakisdimethylaminotitanium (TDMAT) feed (e.g. from a bubbler), followed by an aluminum treatment step. Deposition of CVD TiN by the thermal decomposition of TDMAT results in a layer with good step coverage and low particle counts. The following tables give results from actual test runs using sample embodiments of the process described herein.

|               |                      |   |
|---------------|----------------------|---|
| 5             | Reactor Used:        | AMAT P-5000                                       |
|               | Starting Surface:    | TEOS-deposited oxide                              |
| First stage:  |                      |   |
| 10            | TDMAT Flow:          | 70 sccm   |
|               | N <sub>2</sub> Flow: | 100 sccm  |
|               | Susceptor Temp:      | 460°C   |
| 15            | Total Pressure:      | 120 mTorr   |
|               | Time:                | 30 sec  |
|               | Film Thickness:      | 40 nm   |
| 20            | Composition:         | porous TiCN                                       |
| Second stage: |                      |   |
| 25            | A2 Source Flow:      | 200 sccm of He/AlMe <sub>2</sub> H (from bubbler) |
|               | Diluent Flow:        | 500 sccm of Ar plus 300 sccm of H <sub>2</sub>    |
|               | Susceptor Temp:      | 200 °C  |
|               | Total Pressure:      | 25000 mTorr                                       |
|               | Time:                | 15 sec  |
|               | Film Thickness:      | about 10 nm of A1 over TiAIN                      |

The following table illustrates the results obtained utilizing a third stage consisting of a rapid thermal annealing step, which provided additional stability. Note that the first stage is performed in only 15 seconds as opposed to 30 seconds in the first test run, and the flow of the aluminum source has been reduced by one-half. This embodiment has an improved step coverage over the embodiment shown in the first table.

|               |                      |   |
|---------------|----------------------|---|
| 35            | Sample Embodiment    |   |
|               | Reactor Used         | AMAT P-5000                                       |
|               | Starting Surface:    | TEOS oxide  |
| First Stage   |                      |   |
| 40            | TDMAT Flow:          | 70 sccm   |
|               | N <sub>2</sub> Flow: | 100 sccm  |
|               | Susceptor Temp:      | 460°C   |
| 45            | Total Pressure       | 1200 mTorr  |
|               | Time:                | 15 sec  |
|               | Composition:         | porous Ti-N-C                                     |
| Second stage: |                      |   |
| 50            | Al Source Flow:      | 100 sccm of He/AlMe <sub>2</sub> H (from bubbler) |
|               | Diluent Flow:        | 500 sccm or Ar plus 300 sccm of H <sub>2</sub>    |
|               | Susceptor Temp:      | 175°C   |
|               | Total Pressure       | 25000 mTorr                                       |
| 55            | Time:                | 15 sec  |
| Third Stage   |                      |   |
|               | Anneal Temp:         | 460°C   |

(continued)

| Third Stage |        |
|-------------|--------|
| Time:       | 90 sec |

5

This process resulted in the diffusion barrier shown in the micrograph of Figure 6. Note that the surface is very planar.

10 TiN films deposited directly from TDMAT have been reported to have a very high carbon content. However, this has been found not to be a problem for many metallization applications. Leakage measurements on diodes beneath such a TiN diffusion barrier showed no increased leakage. All plug structures formed using the TiCN containing films have good electromigration resistance.

#### ALTERNATIVE EMBODIMENT: TiN DEPOSITION FROM TDEAT

15 In an alternative embodiment, CVD TiN is deposited by the thermal decomposition of tetrakisdiethylaminotitanium. (TDEAT). Films deposited directly from TDEAT have been reported to have similar instability problems as in depositions from TDMAT. However, the subsequent aluminum treatment steps disclosed herein circumvent the problems associated with using TDEAT as a sole source gas to deposit TiN.

20 **ALTERNATIVE EMBODIMENT: TiN DEPOSITION FROM TMEAT**

25 In another alternative embodiment, CVD TiN is deposited by the thermal decomposition of TMEAT. TiN films deposited directly from TDEAT have been reported to have similar instability problems as in depositions from TDMAT. However, the subsequent aluminum treatment steps disclosed herein circumvent the problems associated with using TDEAT as a sole source gas to deposit TiN.

#### ALTERNATIVE EMBODIMENT: A1 DEPOSITION AT 175°C

30 Low temperatures are preferably used for the aluminum treatment. Preferably the aluminum treatment is performed at 200°C or less; success has been shown at temperatures as low as 175°C. (Other conditions in this experiment were similar to those specified above.)

#### CHARACTERIZATION DATA

35 Figure 3 summarizes x-ray photoelectron spectroscopy (XPS) depth profile data for a TiN + A1 film deposited by the above described process. The data shows chemical compositions as a function of sputtering time (related to depth). As this data shows, the barrier film structure (after exposure to atmosphere) has a surface oxide layer (approximately A1<sub>2</sub>O<sub>3</sub>), over a layer of aluminum metal, over a layer of nitride wherein the aluminum:titanium ratio shifts from 1:0 to 0:1 over a distance of about 500Å, over the body of titanium nitride.

40 The TiN + A1 films deposited according to the process described herein enhance the barrier performance and improve the oxidation resistance of metallization structures, of which various examples are disclosed herein.

#### SAMPLE METALLIZATION EMBODIMENT

45 The process disclosed herein can be utilized in metallization applications, particularly for copper (Cu) metallization. For example, in one application, as shown in Figure 5A, a partially fabricated structure is provided which includes transistors (not shown) beneath a conductor layer 510 (typically in aluminum alloy) which is surrounded by a lower interlevel dielectric 515. Thereafter, an upper interlevel dielectric 520 (e.g. BPSG over TEOS-deposited SiO<sub>2</sub>) is deposited and planarized by conventional methods (e.g. chemical-mechanical polishing, or "CMP"). Thereafter (in a process of the type referred to as a "damsocene") the interlevel dielectric 520 is patterned and etched to form slots 530 where lines of metallization are desired, and also to form deeper holes 540 where vias are desired (i.e. where an electrical contact to the underlying conductor layer is desired). Thereafter a diffusion barrier layer 530 is deposited, using one of the processes described above. A highly conductive metal 550 (e.g. copper) is then deposited overall by conventional methods, and etched back and polished (using e.g. CMP) so that the flat surface of the interlevel dielectric 520 is exposed wherever the metal 550 is not present.

55 Note that, in this embodiment, the barrier layer runs over all exposed portions of the interlevel dielectric 520. That is, there are no locations where metal 550 comes directly in contact with the interlevel dielectric 520. This cuts down on the likelihood of copper atoms (or other lifetime killers such as gold) diffusing through an interlevel dielectric into

the semiconductor substrate.

#### SECOND SAMPLE METALLIZATION EMBODIMENT

5 Another metallization embodiment, as shown in Figure 5B, involved the formation of transistors with polycide gates 560 aligned to source/drain diffusions 562. A first interlevel dielectric layer 564 is subsequently formed. (Optionally, this is often followed by deposition and patterning of additional poly layers with corresponding additional interlevel dielectric layers, but these are not shown.) Contact locations 566 are patterned and etched prior to the deposition of the barrier layer 570 using the processes disclosed herein. The metal layer 580 can then be deposited and patterned.

10 In this sample embodiment, the metal layer 580 is an aluminum alloy, which is forced into the contact hole under superatmospheric pressure (using the "ForceFill™" process, in the presently preferred embodiment.

#### THIRD SAMPLE METALLIZATION EMBODIMENT

15 Yet another metallization embodiment involves the application of TiN + Al films as diffusion barrier for DRAM capacitor application, shown in Figure 5C. After formation of poly-plug 590, the TiN + Al films 592 are deposited on top of the poly-Si plug 590, followed by deposition of an electrode 594 (e.g. of Pt, Ru, or other metal). After patterning the electrode structure, a high dielectric constant oxide film 596 (e.g.  $Ba_xSr_{1-x}Ti_yO_3$ ) is deposited in an oxidizing environment. Without the barrier action of layer, oxygen will diffuse to the electrode/Si interface (592/590) and form  $SiO_x$  film is effectively inserted in series, and reduces the effective capacitance of the capacitor cell. With TiN + Al layer 592 as a barrier, oxygen diffusing through electrode metal layer 594 will form a self-limiting  $Al_{10}O_x$  layer 598 at the interface between electrode metal 594 and TiN + Al diffusion layer. This  $Al_{10}O_x$  layer 598 can very effectively block further diffusion of oxygen.

20 According to a disclosed class of innovative embodiments, there is provided: A film fabrication method, comprising the steps of: (a.) depositing a porous thin film which predominantly comprises TiN; and (b.) after said step (a.) introducing aluminum into pores of said porous thin film and onto the surface thereof; whereby said aluminum on the surface of said porous thin film passivates said porous thin film against atmospheric exposure.

25 According to another disclosed class of innovative embodiments, there is provided: An integrated circuit fabrication method, comprising the steps of: (a) forming transistors; (b) forming an interlevel dielectric layer; (c) patterning and etching said dielectric layer to form holes in contact locations; (d) depositing a porous thin film which predominantly comprises TiN; (e) after said step (d), performing a post-treatment step which introduces aluminum into said porous thin film; and (f) depositing a metal layer.

30 According to another disclosed class of innovative embodiments, there is provided: A thin film comprising: at a topmost surface thereof, an oxide layer which predominantly comprises alumina; and therebelow an aluminum-rich layer having an Al:Ti atomic ratio which is at least 5:1 and therebelow a composition which varies smoothly to a lower layer having a Ti-Al atomic ratio which is at least 5:1.

#### MODIFICATIONS AND VARIATIONS

35 As will be recognized by those skilled in the art, the innovative concepts described in the present application can be modified and varied over a tremendous range of applications, and accordingly the scope of the invention is not limited by any of the specific exemplary teachings given.

40 It should also be noted that the number of layers of metallization described above does not implicitly limit the invention, which can be applied to processes and structures with more or fewer layers.

45 Alternatively,  $AlH_3$  (through  $AlH_3.NEtME_2$ ) or TEA ( $Al(C_2H_5)_3$ ) or other known Al sources can be deposited over the TiN film using CVD processes.  $AlH_3$  is more active and smaller in size than  $SiH_4$  which provides the benefit of easier Al incorporation into the porous TiCN film.

50 Alternatively, an aluminum predeposition step can be performed at low or very low temperatures, and then the wafer can be heated to cause the decomposition of the aluminum precursor.

55 Alternatively and less preferably, other techniques (such as rapid sputter deposition) can be used for initial deposition of a porous TiN layer.

The processes embodying the present invention provide significant advantages in hard coating applications. Coating of bulk articles using the method described herein eliminates the need to provide a protective coating on such articles.

General background on CVD and metallization is found in the following publications, which help to show the knowledge of those skilled in the art regarding variations and implementations: METALLIZATION AND METAL-SEMICONDUCTOR INTERFACES (ed. Batra 1989); VLSI METALLIZATION: PHYSICS AND TECHNOLOGIES (ed. Shenai 1991); Murarka, METALLIZATION THEORY AND PRACTICE FOR VLSI AND ULSI (1993); HANDBOOK OF MULTI-

LEVEL METALLIZATION FOR INTEGRATED CIRCUITS (ed. Wilson et al. 1993); Rao, MULTILEVEL INTERCONNECT TECHNOLOGY (1993); CHEMICAL VAPOR DEPOSITION (ed. M L Hitchman 1993 ); and the semiannual conference proceedings of the Electrochemical Society on CVD; all of which are hereby incorporated by reference.

5

## Claims

1. A method of fabricating a film comprising depositing a porous thin film which predominantly comprises TiN; and after said depositing step, introducing aluminum into pores of said porous thin film and onto the surface thereof; such that aluminum on the surface of said porous thin film passivates said porous thin film against atmospheric exposure.

10

2. The method of Claim 1, further comprising, after said introducing step, performing a post-deposition thermal treatment step.

15

3. The method of Claim 1 or Claim 2, further comprising performing said step of depositing said porous thin film using a thermal decomposition of an amino-complexed titanium compound.

20

4. The method of any preceding Claim, further comprising performing said step of introducing at a temperature of 200°C or less.

25

5. The method of any of Claims 2 to 4, further comprising performing said post-treatment step using a silicon precursor admixed with aluminum.

30

6. A film fabricated according to the method of any preceding Claim.

7. A method of fabricating an integrated circuit, comprising the steps of:

forming at least one transistor;

forming an interlevel dielectric layer;

patterning and etching said dielectric layer to form holes in contact locations;

depositing a porous thin film which predominantly comprises TiN;

performing a post-treatment step which introduces aluminum into said porous thin film; and

depositing a metal layer.

35

8. The method of Claim 7, further comprising after said step of performing said post-treatment step, performing a post-deposition thermal treatment step.

40

9. The method of Claim 7 or Claim 8, further comprising performing said step of depositing said porous thin film using a thermal decomposition of an amino-complexed titanium compound.

45

10. The method of any of Claims 7 to 9, further comprising performing said step of introducing at a temperature of 200°C or less.

50

11. The method of any of Claims 7 to 10, further comprising performing said post-treatment step using a silicon precursor admixed with aluminum.

12. An integrated circuit fabricated according to the method of any of Claims 7 to 11.

55

13. A thin film comprising:

an oxide layer which predominantly comprises alumina, an aluminum-rich layer having an A1-Ti atomic ratio which is at least 5:1;

a composition which varies smoothly to a lower layer having a Ti-A1 atomic ratio which is at least 5:1.

14. The thin film of Claim 13, wherein said aluminum-rich layer comprises at least 50Å of metallic aluminum.

15. The thin film of Claim 13 or Claim 14, wherein said oxide layer forms a topmost surface thereof.

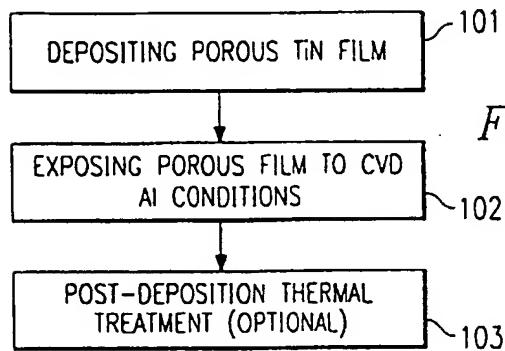


FIG. 1

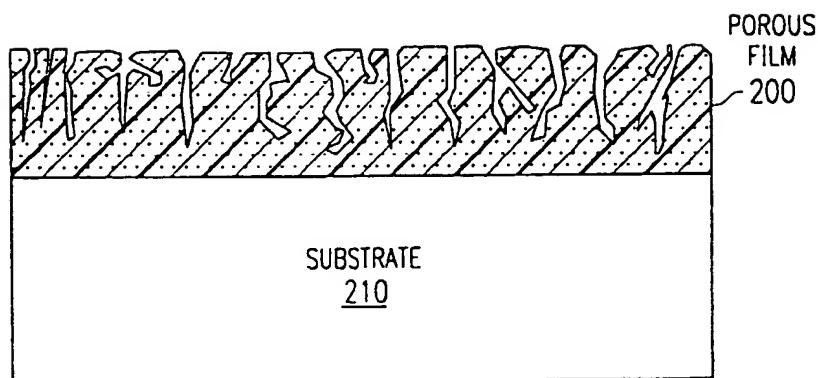


FIG. 2A

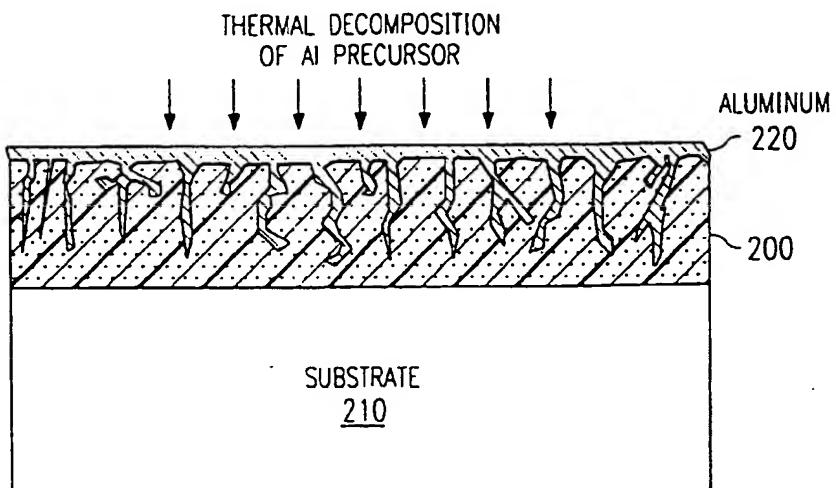


FIG. 2B

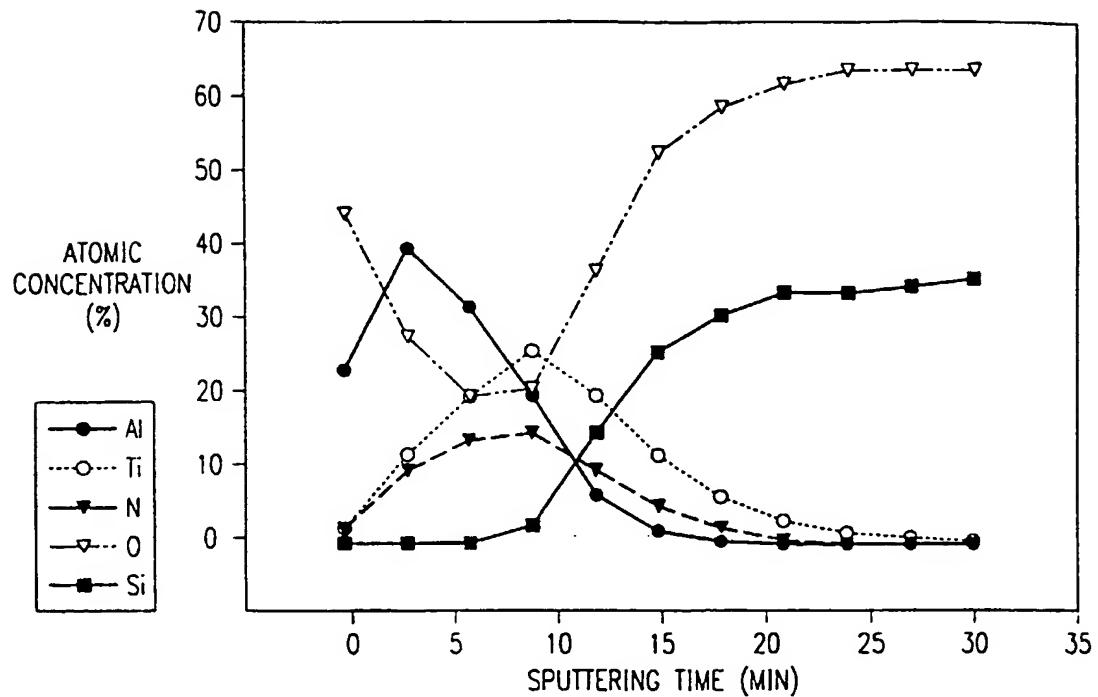


FIG. 3

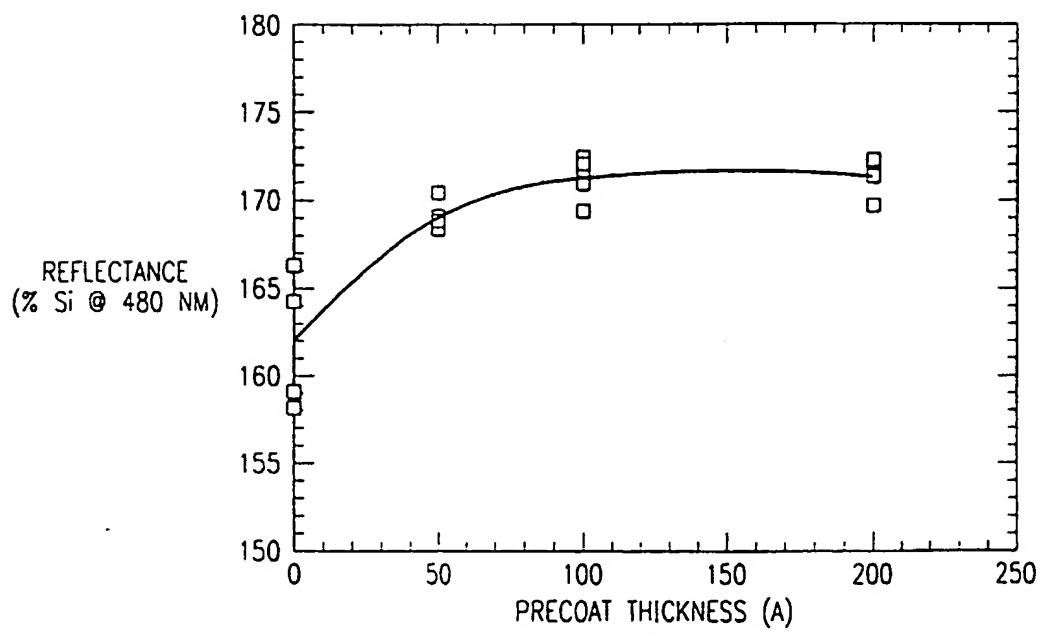
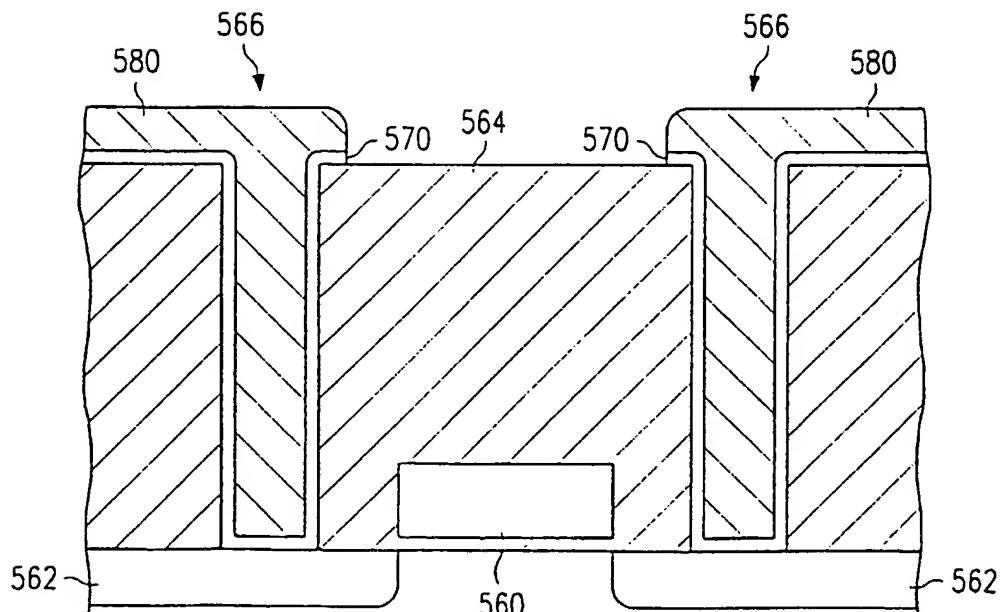
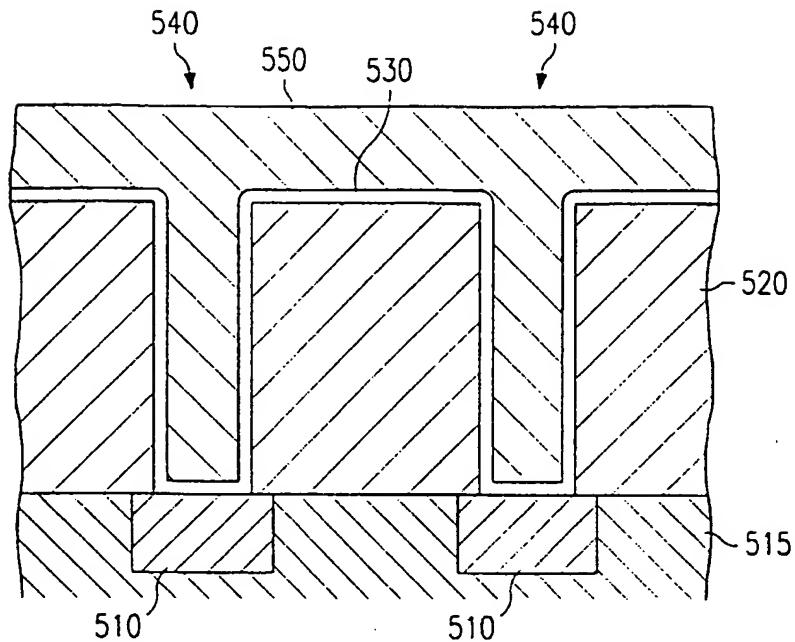


FIG. 4



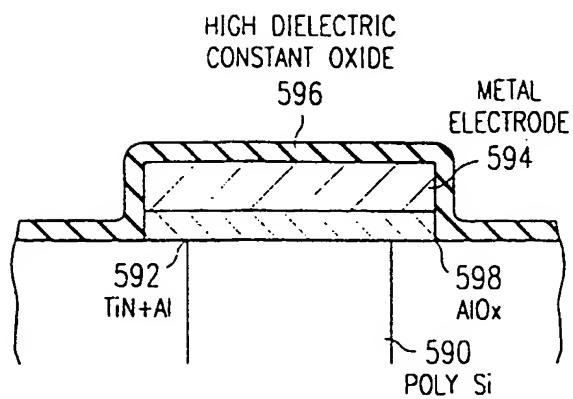


FIG. 5C

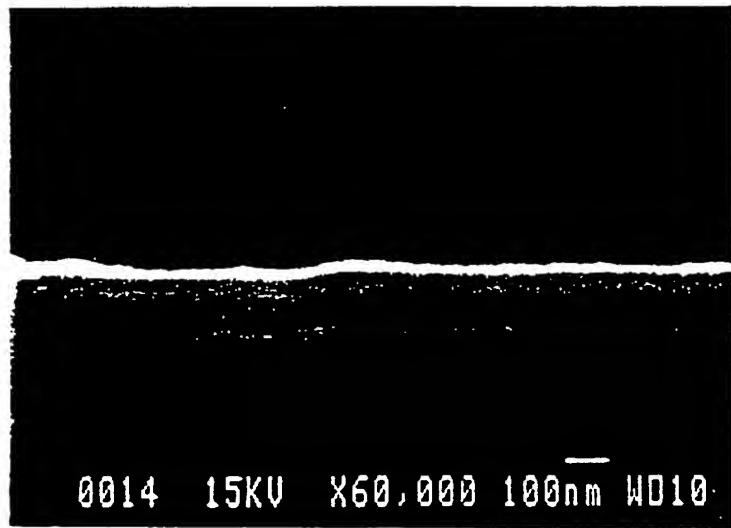


FIG. 6



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(71) Applicant: TEXAS INSTRUMENTS INC.  
Dallas, Texas 75243 (US)

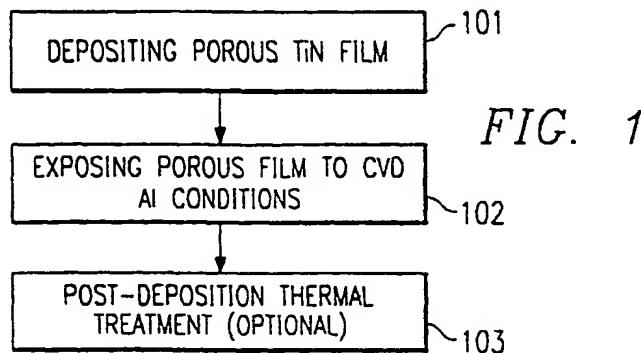
(72) Inventors:  
• Lu, Jiong - Ping  
Dallas Texas 75243 (US)  
• HSU, Wei-Yung  
Dallas Texas 75243 (US)  
• Hong, Qi-Zhong  
Dallas Texas 75243 (US)

(74) Representative: Williams, Janice et al  
D. Young & Co.,  
21 New Fetter Lane  
London EC4A 1DA (GB)

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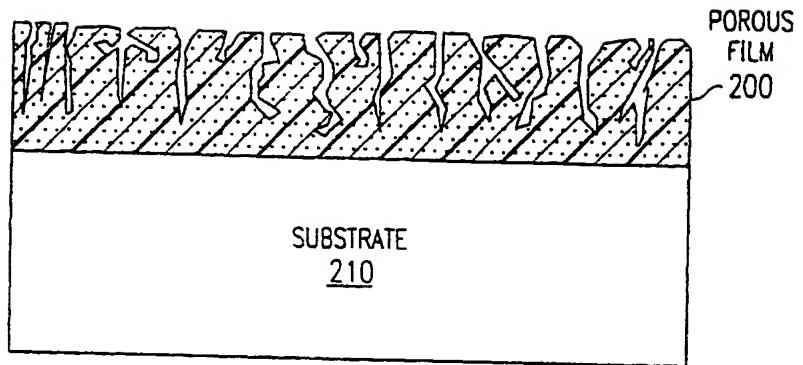


FIG. 2A

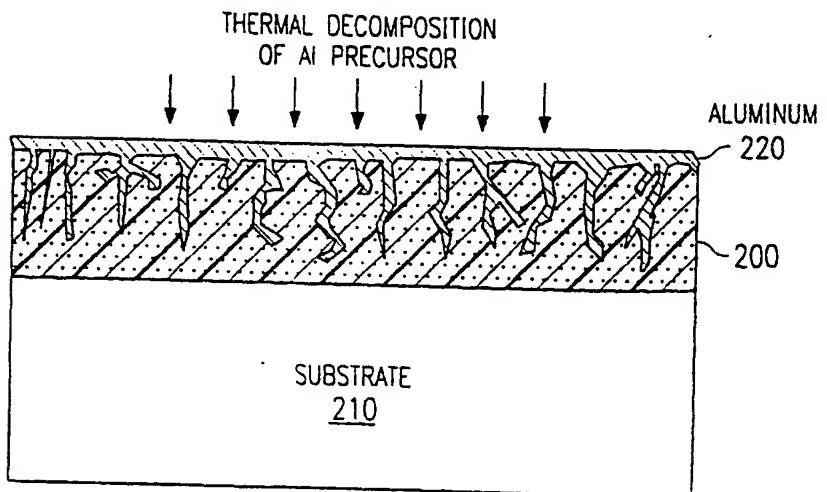


FIG. 2B



European Patent  
Office

## EUROPEAN SEARCH REPORT

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## DOCUMENTS CONSIDERED TO BE RELEVANT

| Category   | Citation of document with indication, where appropriate, of relevant passages  | Relevant to claim         | CLASSIFICATION OF THE APPLICATION (Int.Cl.6)       |  |  |
|--|--|---------------------------|--|--|--|
| X  | DE 195 15 564 A (NIPPON DENSO CO) 2 November 1995<br>* page 2, line 11 - line 20 *<br>* page 2, line 44 - line 68 *<br>* page 3, line 34 - line 47 *<br>* page 4, line 53 - page 5, line 3; figure 1 *<br>* page 5, line 18 - line 29 *<br>* page 5, line 66 - page 6, line 9; figures 5,6 *<br>* page 7, line 55 - page 8, line 39; figures 1-2E *<br>* page 8, line 44 - line 54 *<br>* page 8, line 65 - page 9, line 10 *<br>* page 11, line 12 - line 24; figures 24A,24B *<br>* page 11, line 51 - line 61 * | 1,2,4,6                   | C23C16/34<br>H01L21/285<br>C04B41/51<br>H01L21/768 |  |  |
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| A  | ---  | 7,8,10,<br>12             | TECHNICAL FIELDS<br>SEARCHED (Int.Cl.6)            |  |  |
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| A  | ---  | -/-                       |  |  |  |
| The present search report has been drawn up for all claims   |  |                           |  |  |  |
| Place of search  | Date of completion of the search   | Examiner                  |  |  |  |
| BERLIN   | 28 January 1998  | KLOPFENSTEIN, P           |  |  |  |
| CATEGORY OF CITED DOCUMENTS  |  |                           |  |  |  |
| X : particularly relevant if taken alone<br>Y : particularly relevant if combined with another document of the same category<br>A : technological background<br>O : non-written disclosure<br>P : intermediate document  |  |                           |  |  |  |
| T : theory or principle underlying the invention<br>E : earlier patent document, but published on, or after the filing date<br>D : document cited in the application<br>L : document cited for other reasons<br>& : member of the same patent family, corresponding document |  |                           |  |  |  |



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Application Number

EP 97 30 8768

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| Place of search  | Date of completion of the search  | Examiner          |  |  |  |
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| X : particularly relevant if taken alone   | T : theory or principle underlying the invention  |                   |  |  |  |
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| A : technological background   | D : document cited in the application   |                   |  |  |  |
| O : non-written disclosure   | L : document cited for other reasons  |                   |  |  |  |
| P : intermediate document  | .....<br>& : member of the same patent family, corresponding<br>document  |                   |  |  |  |



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| The present search report has been drawn up for all claims   |  |                   |  |  |  |
| Place of search  | Date of completion of the search   | Examiner          |  |  |  |
| BERLIN   | 28 January 1998  | KLOPFENSTEIN, P.  |  |  |  |
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